


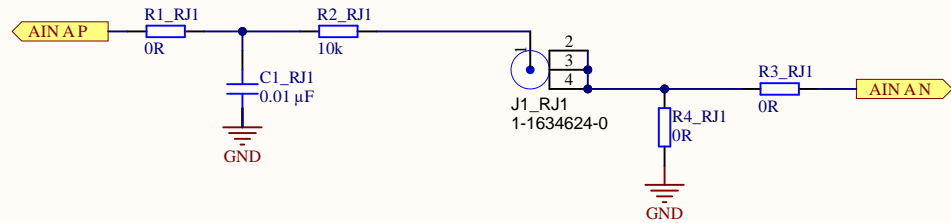
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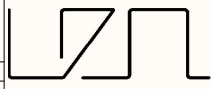
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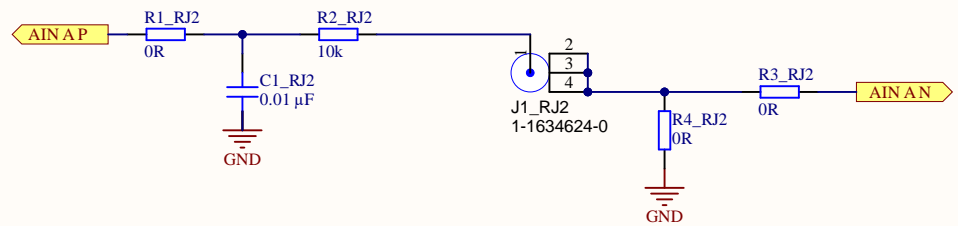
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Revision: Rev01	Design Engineer: Florian Rosner	
Project: UZ_Per_ADC_to_BNC.PrjPcb		Date: 18.03.2022 Sheet 1 of 9



Title: rj45_bnc.SchDoc	
Revision: Rev01	Design Engineer: Florian Rosner
Project: UZ_Per_ADC_to_BNC.PrjPcb	

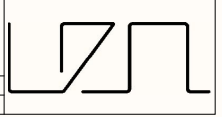
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Sheet 2 of 9

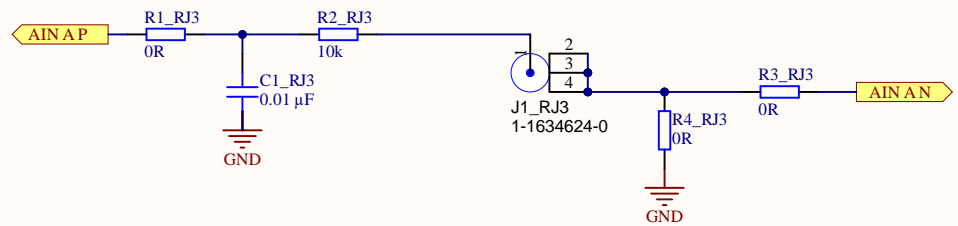




Title: rj45_bnc.SchDoc	
Revision: Rev01	Design Engineer: Florian Rosner
Project: UZ_Per_ADC_to_BNC.PrjPcb	

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Sheet 3 of 9

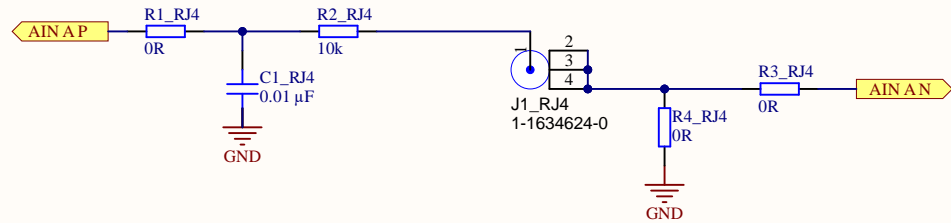




Title: rj45_bnc.SchDoc	
Revision: Rev01	Design Engineer: Florian Rosner
Project: UZ_Per_ADC_to_BNC.PrjPcb	

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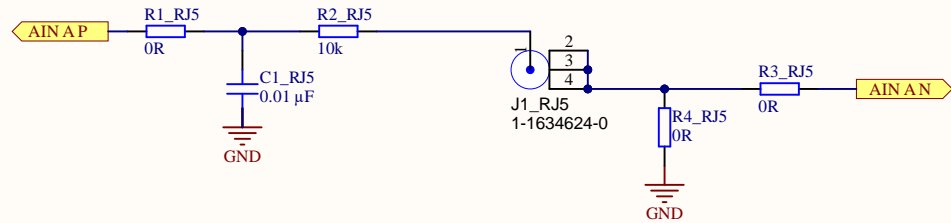




Title: rj45_bnc.SchDoc	
Revision: Rev01	Design Engineer: Florian Rosner
Project: UZ_Per_ADC_to_BNC.PrjPcb	

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Sheet 5 of 9

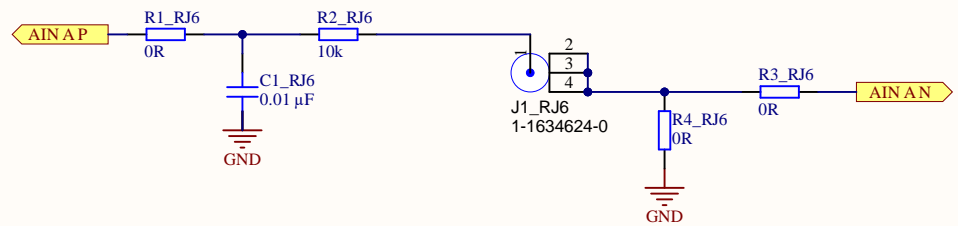




Title: rj45_bnc.SchDoc	
Revision: Rev01	Design Engineer: Florian Rosner
Project: UZ_Per_ADC_to_BNC.PrjPcb	

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 Sheet 6 of 9

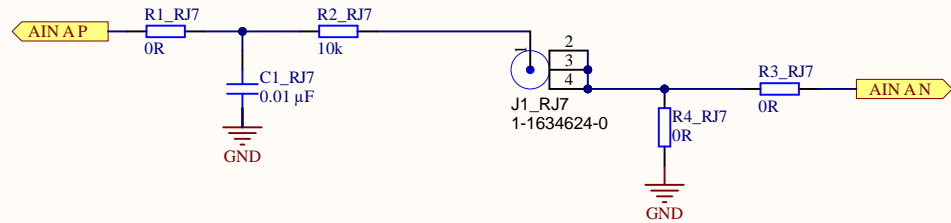




Title: rj45_bnc.SchDoc	
Revision: Rev01	Design Engineer: Florian Rosner
Project: UZ_Per_ADC_to_BNC.PrjPcb	

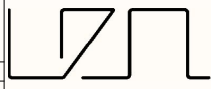
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Sheet 7 of 9

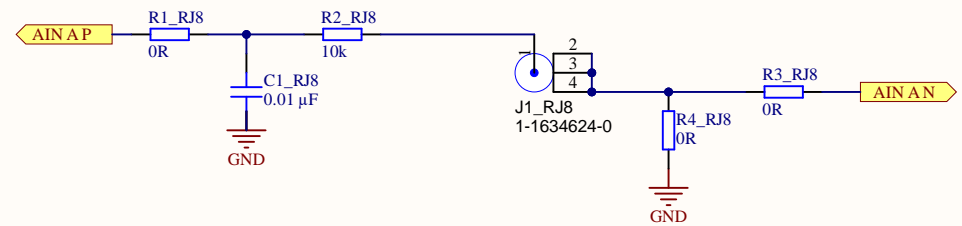




Title: rj45_bnc.SchDoc	
Revision: Rev01	Design Engineer: Florian Rosner
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Sheet 8 of 9





Title: rj45_bnc.SchDoc	
Revision: Rev01	Design Engineer: Florian Rosner
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 Sheet 9 of 9

